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R. Tall

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Patent Application:

Applicant : Leland R. Nevill
Assignee : Micron Technology, Inc.
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED
CIRCUITS
Docket No. : 500060.02

Corresponding Issued U.S. Patent:

Patent No. : 5,984,190
Issued: : November 16, 1999
Application No. : 08/857,100
Filing Date : May 15, 1997
Examiner : Daniel St. Cyr
Art Unit : 2876

BOX REISSUE PATENT APPLICATION
Commissioner of Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Sir:

Please accept the request for a reissue of U.S. Patent No. 5,984,190, filed herewith. Prior to conducting the examination of the reissue claims, please amend the reissue application as follows:

Please add the following new claims 23-34: